

<b>Notice of References Cited</b>	Application/Control No. 09/852,882	Applicant(s)/Patent Under Reexamination EUSTIS, MARY SUSAN HUHN	
	Examiner Patrice Winder	Art Unit 2145	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	NN880741, High Performance/high Availability Interprocessor Communication Method, July 1988, IBM Technical Disclosure Bulletin, volume 31, issue 2, pages 41-42
	V	NB9112190. MIMD Parallel Architecture with Critical Path Acceleration, December 1991, IBM Technical Disclosure Bulletin, volume 34, issue 7B, pages 190-191
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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